	Application No.	Applicant(s)
Notice of Allowability	10/064,398	FORLENZA ET AL.
	Examiner	Art Unit
	JAMES C. KERVEROS	2133
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R	(OR REMAINS) CLOSED in to or other appropriate communing IGHTS. This application is sufficient to the community of the commun	his application. If not included ication will be mailed in due course. THIS
1. $igspace$ This communication is responsive to <u>AMENDMENT After I</u>	Final filed 6/28/2005	
2. X The allowed claim(s) is/are 7-11 and 15, Renumbered as 0	<u>Claims 1-6</u> .	
3. \boxtimes The drawings filed on <u>27 August 2002</u> are accepted by the	Examiner.	
4. ☐ Acknowledgment is made of a claim for foreign priority una) ☐ All b) ☐ Some* c) ☐ None of the: 1. ☐ Certified copies of the priority documents have 2. ☐ Certified copies of the priority documents have 3. ☐ Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)). * Certified copies not received:	e been received. e been received in Application	No
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to file a MENT of this application.	reply complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give	nitted. Note the attached EXAN es reason(s) why the oath or d	MINER'S AMENDMENT or NOTICE OF leclaration is deficient.
6. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.	
(a) Including changes required by the Notice of Draftspers		PTO-948) attached
1) hereto or 2) to Paper No./Mail Date		
(b) including changes required by the attached Examiner' Paper No./Mail Date	s Amendment / Comment or ir	n the Office action of
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on the he header according to 37 CFR	drawings in the front (not the back) of 1.121(d).
7. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT	Sit of BIOLOGICAL MATER FOR THE DEPOSIT OF BIOL	RIAL must be submitted. Note the OGICAL MATERIAL.
	-	
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. Notice of Info	rmal Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview Sun	nmary (PTO-413), ail Date
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date		mendment/Comment
4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ⊠ Examiner's S 9. □ Other	atement of Reasons for Allowance GUY LAMARRE
		PRIMARY EXAMINER

DETAILED ACTION

This is a Notice of Allowance in response to AMENDMENT After Final filed 6/28/2005.

Claims 7-11 and 15 were previously allowed in the prior Office Action.

Claims 1-6 and 12-14 have been cancelled.

Allowable Subject Matter

Claims 7-11 and 15 are allowed. Renumbered as Claims 1-6.

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance:

The prior arts of record taken alone or in combination fail to teach, anticipate, suggest or render obvious the claimed invention, including the claimed features, recited, inter alia, as follows:

Independent claim 7, recites, inter alia, the claimed features of unloading the values of all the latches from the scan chain before the identified failing latch, generating a load from the unloaded values of all the latches from the scan chain before the identified failing latch, applying the generated Load as the first event of an LSSD deterministic pattern by using the same identical primary inputs and clocks that produced the failure in the failing latch in the first semiconductor device to the second semiconductor correctly operating device using a bootstrap technique.

Similarly, independent claim 10 recites, inter alia, the claimed features of observing the states of the logic circuit containing the failed latch by unloading the

values of the plurality of latches from the LSSD scan chain positioned in the scan chain before the identified failing latch which includes reading any embedded circuit memories and other circuit storage elements positioned in the scan chain before the identified failing latch, generating a load from the unloaded states of the latches positioned in the scan chain before the failed latch, applying the generated Load as the first event of a newly created independent LSSD deterministic pattern by using the primary inputs and clock signals identical to the primary inputs and clock signals that produced the failure to a known correctly operating device utilizing logic circuits formed of a plurality of lathes arranged in LSSD scan chain and a plurality of other circuit storage elements identical to the failed device by applying a plurality of primary inputs and clock signals to the device by using a bootstrap technique.

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Consequently, independent claims 7 and 10 are allowed over the prior arts of record. Claims 8, 9 and 15 are directly or indirectly depended upon claim 7, and therefore are allowable, and claim 11 is depended upon claim 10, and therefore is also allowable.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JAMES C. KERVEROS whose telephone number is (571) 272-3824. The examiner can normally be reached on 9:00 AM TO 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on (571) 272-3819. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Date: 20 July 2005

Office Action: Allowance

JAMES C KERVEROS

Examiner

Art Unit 2133

By:

GUY LAMARRE PRIMARY EXAMINER

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